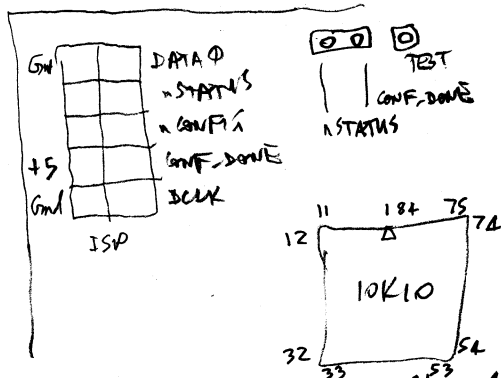


Component Site View



- ~~get PLCC 84 design~~
- ~~get cables~~
- ✓ slip on 2 large shrink tubing on cable
- ✓ slip 1 medium shrink tubing on each twisted pair
- ✓ solder wires to connector
- ✓ shrink tubing
- ✓ tie down each pair

here
20 ga copper

- add GND and +5 test points to ISA proto board, and GND jumpers to Vector proto board
- for logic probe (a multimeter) connections
- borrow multimeter, heatshrink gun, soldering iron
- test voltages at disk drive power cable
- plug power cable to ISA proto board and test voltages on board
- plug vector proto board into ISA proto board & test voltages
- add 3-pin jumper to select DCLK or 14.318 MHz clock (from bus) to global PLD clock
- connect DATAΦ to INPUT line (pin 2)
- connect all unused INPUT lines to GND
- build download cable using 3 twisted pairs: 6/19 = nCONF_DONE, 7/20 = DATAΦ, 8/21 = DCLK
- write Turbo C program to test the download cable - check at PLCC pins
- note test pins for nSTATUS and CONF_DONE
- plug 10K10 chip into socket and test ISP signals
- write Turbo C program to send Ram Binary File to 10K10 via cable

ESC