

'68'

\$2.50 USA

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A \$ 4.00
S \$ 8.00

New Zealand NZ \$ 4.00
Hong Kong H \$20.00

Malaysia

M \$ 8.00

MICRO JOURNAL

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"Small Computers Doing Big Things."

SERVING THE 6800 USERS WORLDWIDE



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A.C. POWER CONTROL for ALL COMPUTERS or COMPLETE TURNKEY SYSTEMS



Interface TO the Real World with GIMIX Relay Driver Boards. Connects to any Computer through a 20 ma. current loop (up to 4 Boards-128 Relays per port).

Interface FROM the Real World with GIMIX

- ★ OPTO BOARDS (up to 34 switch closures with one 8 bit Parallel I/O Port)
- ★ 16 BUTTON KEYPADS
- ★ 35 BUTTON ALPHANUMERIC KEYPADS

A Broad Range of 6800 Systems and Boards Compatible with Other SS50 Products such as SWTP and MSI



MAINFRAME: Includes chassis, power supply, switches, fan and mother board... **\$ 798.19**

16K SYSTEMS: Mainframe, plus 6800 CPU, 16K Static Ram and choice of I/O... **\$1344.29**
Other packages available.

6800/6809 MOTHER BOARD

Hardware reconfigurable to give you the utmost versatility for use with various 6800 and 6809, SS50 and SS50C bus configurations. Gold plated pins to insure long lasting electrical contact for protection against corrosion. Fifteen 50 pin slots plus eight DIP-switch addressable 30 pin I/O slots configurable to 4, 8 or 16 decoded addresses. The fully buffered I/O block is addressable by DIP-switch to any 32, 64 or 128 byte boundary and can also be disabled. An on board baud rate generator provides 9 standard baud rates from 75 to 9600. A 14 pin DIP-header allows easy selection of up to five baud rates. The five baud rate lines on the 50 pin bus can be easily disconnected from the 30 pin bus for use as user defined lines (extended address lines etc.). An optional slow I/O circuit, for the 6809 CPU, allows stretching the clock 1/2 cycle whenever an I/O device is accessed. (This allows, for example, using 1Mhz I/O cards with a 2 Mhz 6809 CPU). UD1 and UD2 of the 50 pin bus can be strapped to UD3 and UD4 of the 30 pin bus. A fully shielded, (.090" thick), double sided P.C. board with noise reducing ground lines on the bottom side that separate all data, address, and signal lines, and a full ground plane on the top side. Schmidt trigger buffering of all address, data, and control signals. A 14 position clamping terminal block for all power and other external connections eliminates soldering, crimping or forming of wires.

TI TMS 4044's — 10% SUPPLY (Not an "equivalent", but the real thing!)

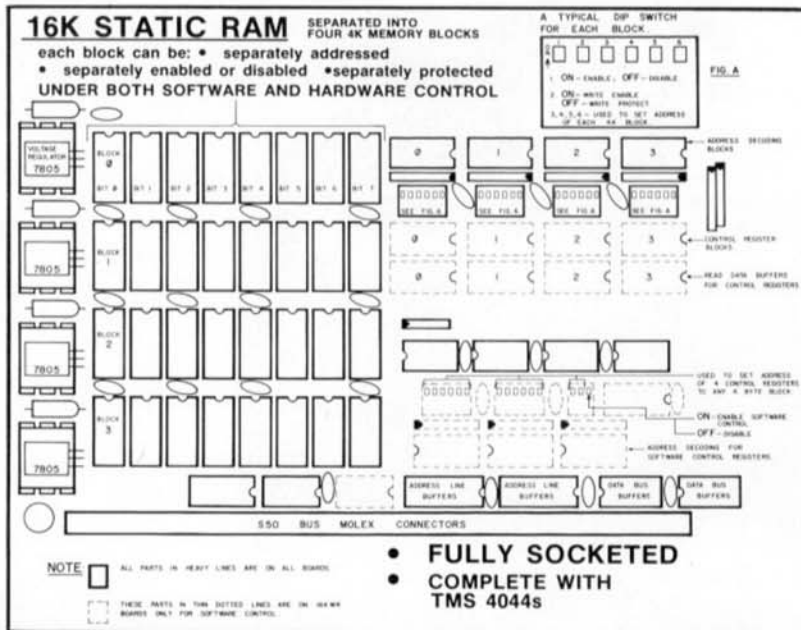
450 ns	\$5.90 each	250 ns	\$6.90 each
8K PROM BOARD			\$ 98.34
4K PPD PROM BOARD, Burner and Duplicator			198.35
2708's FACTORY PRIME 450 ns	each		7.90
64 or 32 x 16 VIDEO BOARD			198.71
80 x 24 SUPER VIDEO BOARD with user programmable RAM character generator			458.76
Serial I/O's	1 Port \$ 88.41	4 Port	198.43
Parallel I/O's	2 Port \$ 88.42	8 Port	198.45

Add \$5. handling charge on orders under \$200.

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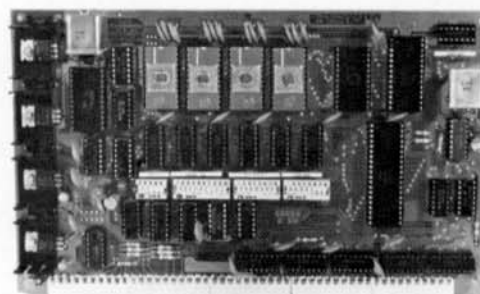


and its also SOFTWARE CONTROLLABLE.....\$368.16

Under software you can program each 4K blocks address and you can enable/disable or write protect each 4K block. The CPU can read the status of the software control registers for each 4K. This allows expansion of memory way beyond 65K; or allows multi-tasking with just one 16K board; all with very little software overhead. (We have calculated that you can put 13,000 of these boards in a system — that's 208 megabytes, but we do not intend to make either a bus or power supply that size.) All registers included — no phantom lines needed.

- ★ Facilitates multiprogramming, time-sharing, and software development.
- ★ All Gimix memory boards are assembled, burnt-in for 2 weeks, and tested at 2Mhz.

AS ABOVE, BUT WITHOUT SOFTWARE CONTROL AND UNSOCKETED.....\$298.13



GIMIX 6800 CPU BOARD

- ★ 6800 MPU
- ★ 4K EPROM
- ★ 3 Programmable timers
- ★ 128 byte RAM
- ✓ DIP-switch addressing and enabling or disabling for software use versatility.

This board features:

- ✓ Crystal controlled 6800 MPU using a 6875 clock generator
- ✓ 14411 bit-rate generator with its own crystal that provides baud rates from 110 to 9600.
- ✓ 128 Byte 6810 SCRATCHPAD RAM, which can be DIP-switch addressed to any 128 byte boundary, or disabled.
- ✓ Gold Bus Connectors

Buffered for reliability and data integrity All address output, data input and output, halt input, clock, reset, control and baud rate output lines are buffered. Manual reset input line is buffered and debounced. IRQ and NMI lines are direct in (6.8K pullup) DMA capability through cycle stealing or halt.

A 4K ROM SECTION THAT HAS:

Sockets for 4 2708 EPROMS which can be DIP-switch addressed to any 4K boundary, or disabled. Dual-address switch lets one PROM respond to both E000 and FC00, for MIKBUG compatibility. Split-address strapping places PROMS at E000, E400, E800, FC00 if desired.

Plus Three Programmable Timers (optional)

Eliminate the need for software timing loops. A 6840 software programmable timer provides 3 independent 16-bit counters which may be used to cause interrupts and/or generate output signals. They can be used individually or in combination. They may be programmed for one-shot timing or for regular intervals. Applications for the timers include frequency measurements, event counting, interval measuring, and similar tasks. They can be used for square wave generation, gated delay signals, single pulses of controlled duration, and pulse width modulation. DIP-switch addressable to any 8 byte boundary. Additional DIP-switch positions control IRQ or NMI interrupt choices, as well as enabling or disabling the timers.